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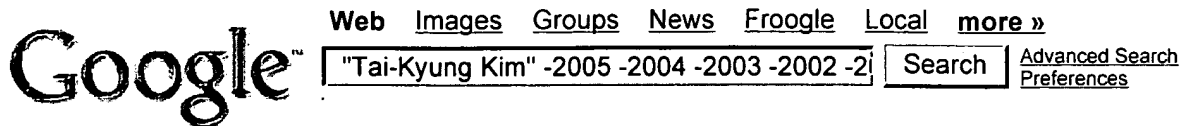
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11	INZZ	oh-j\$	unrestricted	510	<a href="#">show titles</a>
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**Tai-Kyung KIM**. Senior VP, Marketing Group Rutgers University, BA in Economics, Rutgers University, BS in Mechanical Engineering Rutgers University, MBA ...  
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Name: Mr. **Tai Kyung Kim** Position: President. Company: Korea Techno-Venture Ltd.  
 Address: 11th Floor, Jungwoo Building City/postal code: Seoul, 110-051 ...  
[sphere.rdc.puc-rio.br/parcerias/untpdc/eto/etoware/korea45.html](http://sphere.rdc.puc-rio.br/parcerias/untpdc/eto/etoware/korea45.html) - 195k - [Cached](#) - [Similar pages](#)

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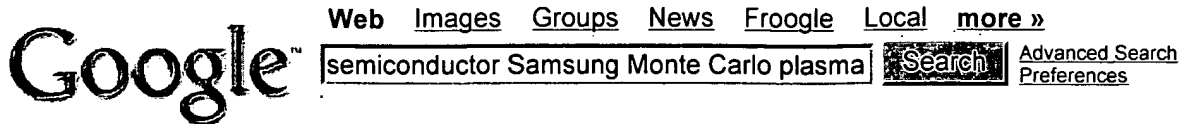
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 Won-Young Chung, Jae-Joon Oh, **Tai-Kyung Kim**,. Jai-Kwang Shin, Kang-I11. Seo\*,  
 Young-Kwan Park, ... Kyusang Lee and **Tai-Kyung Kim**\*, SAIT, Suwon, ...  
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... Shipley Corporation: **Samsung: Semiconductor** Research Coporation: Teltech: Trace  
... DA Fisher, BE Thompson, and HH Sawin, "**Monte Carlo** Modeling of Ion ...  
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... of Stress Conditions for Gate Oxide Wearout Using **Monte Carlo** Simulation",  
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#### RTP Conference 1995

EZ Liu, HM Li, HL Chew, YS Lin, L. Chan, Chartered **Semiconductor** ... The Application  
of **Monte Carlo** and Finite Element Simulation for Optimization of an RTP ...  
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#### Affiliations A AMI Semiconductor, Belgium Use of Oxynitride ...

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Simulation of Tunnel Currents in MOS Structures ...  
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#### Scientific Program Plenary Invited Papers D01 - Nanoscale System ...

D7.1 - Efficient **Monte Carlo** Simulation of Tunnel Currents in MOS Structures ...  
**Semiconductor** R&D center, **Samsung** Electronics, Kyungki-Do, Korea (South) ...  
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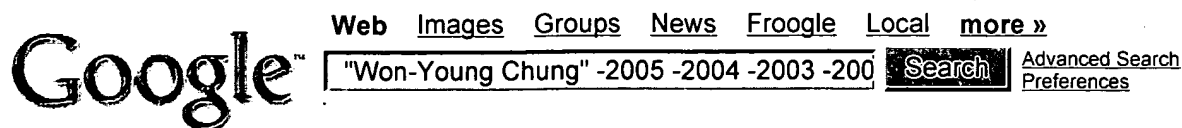
... Nitride **Semiconductor** Team, **Samsung** Advanced Institute of Technology, Suwon,  
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#### Technical Program Thursday, September 1 Session 1: Plenary Session ...

Vienna, Austria and 2**Samsung** Advanced Inst. of Technol., Korea ... Quantum Effects  
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[PDF] 6th International Conference on VLSI and CAD - VLSI and CAD, 1999 ...

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Tai-Kyung Kim, **Won-Young Chung**, Hong-Jae Shin, Jai-Jun Oh, Jai-Kwang Shin, .

Jeong-Taek Kong (Samsung Electronics, Korea). Deposition (IMD) Process . ...

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**Won-Young Chung**, Jae-Joon Oh, Tai-Kyung Kim, . Jai-Kwang Shin, Kang-I11. Seo\*, .

Young-Kwan Park, . and Jeong-Taek Kong, . Samsung Electronics Co., ...

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Joshua Lamkin. Silver. GA. Czrnest De La Cruz. Bronze IN. John Fenton, III ...

www.ustu.org/joform2001.PDF - [Similar pages](#)

### Personal info for suni00

Personal info for suni00. Name: **Won-young Chung**. Homepage: <http://kernel.pe.kr>.

Notes: Translated Configure.help in Korea. Recent diary entries for suni00: RSS. ...

advogato.org/person/suni00/ - 2k - Supplemental Result - [Cached](#) - [Similar pages](#)

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... Professors Dong-Won Bae, Jae-Eun Lee, Hun Yoo, Ki-Heung Kim Associate Professor

**Won-Young Chung** Assistant Professors Hun-Dae Lee, Min-Ho Shin, Hee-Yul Chai ...

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### INSPEC - 1969 to date (INZZ)

#### Accession number & update

4890416, A9506-5265-005; 950308.

#### Title

Simulations of radical and ion transport in inductively coupled **plasma** etching reactors.

#### Author(s)

[Ventzek-P-L-G](#); [Hoekstra-R-J](#); [Kushner-M-J](#).

#### Author affiliation

Dept of Electr &amp; Comput Eng, Illinois Univ, Urbana, IL, USA.

#### Source

International Conference on **Plasma** Sciences (ICOPS), Vancouver, BC, Canada, 7-9 June 1993.  
Sponsors: IEEE Nucl & **Plasma** Sci. Soc., Phys. Dept., Univ. British Columbia.  
In: p.149.

#### ISSN

ISBN: 0-7803-1360-7.

#### Language

EN.

#### Publication type

CPP Conference Paper.

#### Treatment codes

T Theoretical or Mathematical.

#### Abstract

Summary form only given. Inductively coupled **plasma** (ICP) sources are being investigated for use in etching of semiconductors and deposition of polymers. A 2-D hybrid model has been developed to investigate these ICP reactors. The simulation consists of coupled electromagnetic, Monte Carlo, and fluid modules. The electromagnetic module solves for the antenna-generated azimuthal electric field in the **plasma**. The Monte Carlo simulation produces source functions for electron impact processing using both these fields and time-varying electrostatic fields obtained from the fluid module. The fluid module solves the **continuity** equations for charged and neutral species, and **Poisson's** equation while including RF biasing of the substrate. Results for densities and fluxes of ions and radicals to the substrate have been obtained for electropositive (e.g., Ar, He) and electronegative (He/CF/sub 4/, O/sub 2/, Ar/O/sub 2//CF/sub 4/) gas mixtures. (0 refs).

#### Descriptors

[Monte-Carlo-methods](#); [plasma-density](#); [plasma-devices](#); [plasma-simulation](#); [plasma-transport-processes](#); [sputter-etching](#).

#### Keywords

radical transport; tetrafluoromethane Ar O2; **semiconductor** etching; polymer deposition; He

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**INSPEC - 1969 to date (INZZ)**
**Accession number & update**

5297489, A9614-7230-004, B9607-2560B-018, C9607-7320-143; 960618.

**Title**
Simulation of **semiconductor plasma** on multiprocessor computer systems.
**Author(s)**
Chetverushkin-B-N; Ed. by Ryzhii-V; Hamaguchi-C; Mitin-V; Ershov-M.
**Author affiliation**

Inst for Math Modelling, Acad of Sci, Moscow, Russia.

**Source**

Proceedings International Workshop on Physics and Computer Modeling of Devices Based on Low-Dimensional Structures, Aizu-Wakamatsu, Japan, 7-9 Nov. 1995.

Sponsors: Univ. Aizu, Osaka Univ., Wayne State Univ.

In: p.43-6, 1996.

**ISSN**

ISBN: 0-8186-7321-4, CCCC: 0 8186 7321 4/96/ (\$05.00).

**Publication year**

1996.

**Language**

EN.

**Publication type**

CPP Conference Paper.

**Treatment codes**

T Theoretical or Mathematical; X Experimental.

**Abstract**

This paper discusses the algorithms for numerical simulation of **semiconductor** devices (in particular using quasi-hydrodynamic model) on multiprocessor computer systems with distributed memory. Parallel implementation of iterative alpha - beta solver is presented, as well as the method of combined solving the **Poisson** equation and two parabolic equations (current **continuity** and energy balance equations). (3 refs).

**Descriptors**

electronic-engineering-computing; iterative-methods; parallel-algorithms; physics-computing; plasma-simulation; semiconductor-device-models; semiconductor-plasma.

**Keywords**

**semiconductor plasma**; numerical simulation; quasi hydrodynamic model; multiprocessor computer systems; distributed memory; parallel algorithms; parallel implementation; iterative alpha beta solver; **Poisson** equation; parabolic equations; current **continuity**; energy balance; electron hole **plasma**;

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**INSPEC - 1969 to date (INZZ)**

#### Accession number & update

2692942, A86076641, B86041585; 860000.

#### Title

AC transport in short n/sup +/nn/sup +/ layers.

#### Author(s)

[Sinkkonen-J](#); [Eranen-S](#).

#### Author affiliation

Electron Phys Lab, Helsinki Univ of Technol, Espoo, Finland.

#### Source

Physica-Status-Solidi-B (East Germany), vol.134, no.1, p.391-8, 1 March 1986.  
Translation in: C19.

#### CODEN

PSSBBD.

#### ISSN

ISSN: 0370-1972.

#### Publication year

1986.

#### Language

EN.

#### Publication type

J Journal Paper.

#### Treatment codes

T Theoretical or Mathematical.

#### Abstract

The admittance of n/sup +/nn/sup +/ **semiconductor** structures is studied by solving self-consistently the **Poisson** equation, the **continuity** equation, and the linearized Boltzmann equation in the relaxation time approximation for a spatially inhomogeneous electron system. Exact numerical results and an approximate, analytical solution are presented. In addition to the normal bulk admittance and the geometric capacitance of the central n region, the equivalent circuit of the structure consists of contact contributions, the magnitude of which depends on the length of the n region L. At low frequencies the effect of the contacts is to induce a conductance  $G_{\text{c}}$  and a capacitance  $C_{\text{c}}$  in parallel with  $G_{\text{c}}$  in the equivalent circuit. On the other hand, above the **plasma** frequency of the n region the contact admittance vanishes because of the shunting of the capacitive elements. (14 refs).



**Descriptors**semiconductor-junctions.**Keywords**

AC transport; short n nn layers; admittance; n nn **semiconductor** structures; **Poisson** equation; **continuity** equation; linearized Boltzmann equation; relaxation time approximation; spatially inhomogeneous electron system; equivalent circuit; contact contributions; conductance; capacitance.

**Classification codes**

A7340L (**Semiconductor-to-semiconductor** contacts, p-n junctions, and heterojunctions).

B2530B (**Semiconductor** junctions).

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